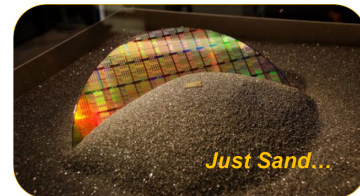# imperas

Revolutionizing Embedded Software Development

Imperas Newsletter: February 2016

"Silicon without software is just sand."

Updating you on what's new in the embedded software revolution.

Just Sand...

Viewpoint: Simon Davidmann, CEO Imperas

Our New Year is off to a great start, and we look forward to seeing many of you at three upcoming industry events: Embedded World in Nuremburg in February, DVCon in Silicon Valley in February, and DATE in Dresden in March.

Please **follow us**: on Linked In, and twitter @ImperasSoftware

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See Imperas at Embedded World 2016

embeddedworld 2016
Exhibition&Conference
...it's a smarter world

Imperas will attend Embedded World in Europe

Larry Lapides, Imperas vice president of sales, will be attending Embedded World 2016 in Nuremberg, Germany. Please contact him at sales@imperas.com to arrange a meeting and demonstration at Embedded World. Also, Imperas partner eSOL will be exhibiting at Embedded World, booth 4-157. eSOL is the leading provider of real-time embedded software solutions, and Imperas tools support the eSOL operating systems and are integrated with the eSOL IDE. Please contact eSOL at ep-info@esol.co.jp to arrange a meeting or demonstration.

Read more here.

See Imperas at DVCon 2016

2016
DESIGN AND VERIFICATION
DVCON
CONFERENCE AND EXHIBITION
UNITED STATES

Imperas CEO Simon Davidmann will speak on ESL panel at DVCon 2016

The DVCon panel, "Redefining ESL", moderated by Brian Bailey of Semiconductor Engineering, addresses ESL for both hardware and software. Join Brian Bailey, Simon Davidmann, and other experts who will attempt to define ESL verification, from tools to flows. Specifically,

Simon will address where ESL and verification needs to go, virtual platforms, embedded software development, debugging, analysis, and verification.

- Where: DoubleTree Hotel, San Jose, CA.
- When: DVCon: February 29 - March 3, 2016. Redefining ESL panel: Wednesday March 2, 8:30 - 9:30am.

Please contact sales@imperas.com to set up a meeting at DVCon 2016.

Read more here.

See Imperas at DATE 2016
Imperas will give a tutorial at DATE (Design, Automation & Test in Europe)

At DATE, Imperas will present on embedded software development, debugging, analysis, and verification with virtual platforms supporting today's multiprocessor SoCs, as part of the tutorial "Internet-of-Things: Virtual Platforms in the Internet-of-Things Era – State of the art and perspectives." This tutorial will give insights into which changes to expect in new virtual platforms regarding efficient CPU simulation, analog-mixed-signal modeling, simulation of extra-functional properties and network simulation, and how these changes can help to design tomorrow's embedded systems more efficiently.

- Where: The International Congress Center (ICC) in Dresden, Germany.
- When: DATE is March 14–18, 2016. The tutorial is March 14, 2016, 14:30-18:00 PM.

Please contact sales@imperas.com to set up a meeting at DATE 2016.

Read more here.

How Much Software can a Virtual Platform Run?

Software Performance Blog from Imperas for Imagination Technologies

Software verification is center-stage today, driving virtual platforms adoption as a mainstream methodology for embedded software development, debug and test. If you haven't used virtual platforms (software simulation) yet, you may be wondering...

Read more here.

Imperas in the News:

"Bridging Hardware and Software in System-Level Design", with Ann Steffora Mutschler in Semiconductor Engineering.

- Read Part 1 here.
- Read Part 2 here.

"Debug: Last Bastion of Automation", experts at the table with Brian Bailey. Read more here.

"More Uses For Hypervisors", in Semiconductor Engineering, with Simon Davidmann. Read more here.

OVPsim Release News

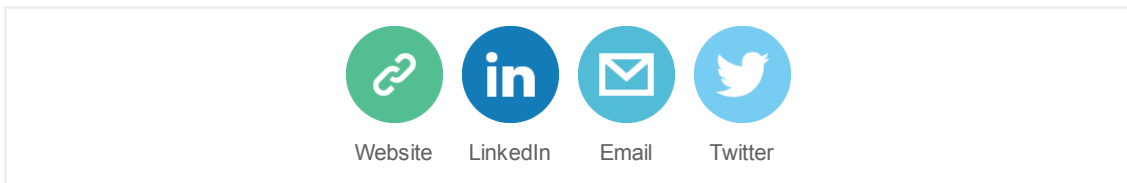**OVP: Fast Simulation, Free Open Source Models, Public APIs: Open Virtual Platforms.**

The Open Virtual Platforms™ (OVP™) portal is one of the most exciting open source software developments in the embedded software world since GNU created GDB.

- For embedded software developers, virtual platforms will be increasingly important, especially for multi-core designs. The resources on this portal can significantly accelerate your development and test.
- Explore what is new at OVPworld!

The current OVPsim release is 20151203.0 (December 2015)

- Please check the release notes on OVPworld.org for more details.
- The next release of OVPsim is expected to be available in March 2016.

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