![imperas logo] **Revolutionizing Embedded Software Development**

Imperas Newsletter: May 2018

"Silicon without software is just sand."

Silicon without Software... Is Just Sand...

Imperas Privacy Policy

We have always taken data protection seriously, and you can trust us to respect your privacy and keep your personal information safe. The new General Data Protection Regulation (GDPR) comes into force May 25, is aimed at giving individuals in the EU more control over the data held by companies, and imposes additional obligations on organizations for how your data is used. Your contact details are securely held within our records either because you have previously signed up to our website, subscribed to one of our newsletters, responded to an event invitation or otherwise been in touch with us. We have been sending you newsletters, event invitations and similar email messages in the past, and our records tell us that you have previously consented or not opted-out from receiving such information. We do not sell or share your data with any third parties. See the Imperas Privacy Policy here.

There is no action required, but we wanted to inform you of the changes and reassure you that our privacy policy complies with the new GDPR regulations. We're absolutely committed to reflecting the high standards set by GDPR; we respect your personal data, and are open and transparent about how we use it.

We would like to stay in touch, but if you would like to stop receiving information from us you are welcome to opt-out or update your profile here: Update your preferences or unsubscribe from this list. We will continue to include unsubscribe links in our emails so you may opt out at any time in the future.

EVENTS

See Imperas at DAC 2018

Imperas will participate in the Design Automation Conference (DAC) 2018, and invites developers of electronic products to visit us there! Please email info@imperas.com to set up a meeting or demo of Imperas virtual platforms for embedded software and systems development, debug and test, at DAC!

DAC 2018 EXHIBIT: Imperas will show virtual platform solutions for design, debug and test at the Imperas pod in the RISC-V booth, #2638.
DEMO HIGHLIGHTS: See Imperas virtual platforms and Open Virtual Platforms (OVP) models for embedded software development, debug, analysis, and verification, featuring RISC-V.

WHERE: Moscone Convention Center, San Francisco, CA.
WHEN: June 24-28, 2018. Exhibits are open June 25-27, 10am to 6pm.

Read more here.

Imperas at RISC-V Workshop Barcelona

Imperas participated in the RISC-V Workshop Barcelona in May in Spain. If you missed it, you can view the videos here:

- A Common Software Development Environment for Many-core RISC-V based Hardware and Virtual Platforms
- RISC V Virtual Platforms for Early RISC V Embedded Software Development

NEWS

Imperas and Andes Extend Partnership, Delivering Models and Virtual Platforms for Andes RISC-V Cores with New AndeStar V5m Extensions

"The momentum for RISC-V is accelerating, and Andes is the first established CPU intellectual property (IP) vendor to offer a RISC-V processor for licensing. We are pleased to support their new AndeStar V5m extensions in the OVP models of their 32-bit/64-bit CPU cores, based on RISC-V," said Simon Davidmann, president and CEO of Imperas.
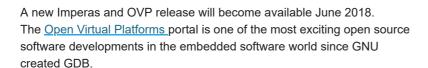Read more here.

New MIPS I7200 Processor Core Delivers Unmatched Performance and Efficiency for Advanced LTE/5G Communications and Networking IC Designs

"Imperas and OVP simulators, virtual platforms, and debug and analysis solutions help accelerate software development for multi-core and multi-threaded I7200 configurations, and make it easy to model new Instruction Set Architectures (ISAs)," said Simon Davidmann, CEO of Imperas Software, Ltd. "MIPS' partners can benefit from our programmers' view reference model, with new I7200 features and nanoMIPS ISA."
Read more here.

OVPsim RELEASE NEWS

OVP: Fast Simulation, Free open source models, Public APIs: Open Virtual Platforms.

A new Imperas and OVP release will become available June 2018. The Open Virtual Platforms portal is one of the most exciting open source software developments in the embedded software world since GNU created GDB.

- For embedded software developers, virtual platforms are increasingly important, especially for multi-core designs.

The resources on this portal can significantly accelerate your development and test. The next release of OVPsim is expected to be available in August 2018.